

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE

Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore™
RELEASE 1.4Welcome
United States Patent and Trademark Office[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#) [Quick Links](#)

Welcome to IEEE Xplore™

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account

 Print FormatYour search matched **37** of **795406** documents.Results are shown **15** to a page, sorted by **publication year** in **descending** order.You may refine your search by editing the current search expression or entering a new one the text box. Then click **Search Again**.

cmos <and> ferroelectric <and> capacitor

Search Again**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****31 A new electrode technology for high-density nonvolatile ferroelectric (SrBi/sub 2/Ta/sub 2/O/sub 9/) memories***Bo Jiang; Balu, V.; Tung-Sheng Chen; Shao-Hong Kuah; Lee, J.C.; Chu, P.Y. R.E.; Zurcher, P.; Taylor, D.J.; Kottket, M.L.; Gillespie, S.J.*VLSI Technology, 1996. Digest of Technical Papers. 1996 Symposium on , 19
Page(s): 26 -27[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) **CNF****32 A ferroelectric capacitor over bit-line (F-COB) cell for high density nonvolatile ferroelectric memories***Tanabe, N.; Matsuki, T.; Saitoh, S.; Takeuchi, T.; Kobayashi, S.; Nakajima, Maejima, Y.; Hayashi, Y.; Amanuma, K.; Hase, T.; Miyasaka, Y.; Kunio, T.*VLSI Technology, 1995. Digest of Technical Papers. 1995 Symposium on , 19
Page(s): 123 -124[\[Abstract\]](#) [\[PDF Full-Text \(300 KB\)\]](#) **CNF****33 Integration of ferroelectric capacitor technology with CMOS***Moazzami, R.; Maniar, P.D.; Jones, R.E.; Mogab, C.J.*VLSI Technology, 1994. Digest of Technical Papers. 1994 Symposium on , 19
Page(s): 55 -56[\[Abstract\]](#) [\[PDF Full-Text \(164 KB\)\]](#) **CNF****34 A microprocessor interface for ferroelectric capacitor memory***Fouts, D.J.; Gonter, T.C.*

Signals, Systems and Computers, 1992. 1992 Conference Record of The Two